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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Nanba Hiromi et al.

Art Unit: 2816

Application No.: 09/921,578

Examiner: Ninh T. Nguyen

Filed: August 6, 2001

Atty. Dkt. No.: 107346-00017

For: LOW POWER CIRCUIT WITH PROPER SLEW RATE BY AUTOMATIC
ADJUSTMENT OF BIAS CURRENT

RESPONSE UNDER 37 CFR §1.121

Commissioner for Patents
Washington, D.C. 20231

July 29, 2002

Sir:

In response to the outstanding Office Action dated March 27, 2002, the time for reply having been duly extended until July 27, 2002 (a Saturday) by the attached petition, please amend the application as follows:

IN THE CLAIMS:

Please amend claims 1-6 as follows:

1. (Amended) A semiconductor integrated circuit comprising:
- an adjusted circuit in which a first bias current flows, a slew rate of said adjusted circuit being dependent on said first bias current:

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